

**PALM INTRANET**

Day : Saturday  
 Date: 10/30/2004  
 Time: 16:42:24

**Inventor Name Search Result**

Your Search was:

Last Name = YANG

First Name = JICHENG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60468420</a>	Not Issued	159	05/07/2003	CERAMIC SUBSTRATE ASSEMBLY PROCESS USING A CARRIER	<b>YANG, JICHENG</b>
<a href="#">10865096</a>	Not Issued	018	06/09/2004	SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD	<b>YANG, JICHENG</b>
<a href="#">10852316</a>	Not Issued	041	05/24/2004	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	<b>YANG, JICHENG</b>
<a href="#">10852315</a>	Not Issued	041	05/24/2004	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	<b>YANG, JICHENG</b>
<a href="#">10845983</a>	Not Issued	041	05/14/2004	COUPLING SPACED BOND PADS TO A CONTACT	<b>YANG, JICHENG</b>
<a href="#">10666345</a>	Not Issued	030	09/18/2003	MULTI-CHIP MODULE WITH EXTENSION	<b>YANG, JICHENG</b>
<a href="#">10215571</a>	<a href="#">6580160</a>	150	08/08/2002	COUPLING SPACED BOND PADS TO A CONTACT	<b>YANG, JICHENG</b>
<a href="#">10144905</a>	<a href="#">6807218</a>	150	05/13/2002	LASER MODULE AND OPTICAL SUBASSEMBLY	<b>YANG, JICHENG</b>
<a href="#">09941315</a>	<a href="#">6620648</a>	150	08/29/2001	MULTI-CHIP MODULE WITH EXTENSION	<b>YANG, JICHENG</b>
<a href="#">09703920</a>	<a href="#">6759307</a>	150	11/01/2000	METHOD TO PREVENT DIE ATTACH ADHESIVE CONTAMINATION IN STACKED CHIPS	<b>YANG, JICHENG</b>
<a href="#">09606480</a>	<a href="#">6274930</a>	150	06/29/2000	MULTI-CHIP MODULE WITH STACKED DICE	<b>YANG, JICHENG</b>
<a href="#">09437870</a>	<a href="#">6453547</a>	150	11/10/1999	COUPLING SPACED BOND PADS TO A CONTACT	<b>YANG, JICHENG</b>
<a href="#">09437595</a>	<a href="#">6207467</a>	150	11/10/1999	MULTI-CHIP MODULE WITH STACKED DICE	<b>YANG, JICHENG</b>

SEARCH ID	SEARCH DATE	SEARCHER
09420817	Not Issued 163 10/19/1999	MULTI-CHIP MODULE WITH EXTENSION YANG , JICHENG

Inventor Search Completed: No Records to Display.

**Search Another:**  
**Inventor**

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<input type="text" value="YANG"/>	<input type="text" value="JICHENG"/>

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## Application

Application No. \* 10666345 Patent No. Issue Date Filing Date 09/18/2003

Examiner No 78780 PERKINS, PAMELA E

GAU 2822 Class 438 Subclass 106000 Status 40 Abandoned

Inventor Name YANG, JICHENG FAOM Y Disposal N

Title of Invention Multi-chip module with extension

## Case History

No	Short Description	Action Date	Action Status	Application		
				Status	Dispose	FAOM
23.00	Count NF Rej	11/01/2004	WFML	40	0.0	1.0 awalker1
22.00	Case Docketed	10/19/2004	COMP	0	0.0	0.0 azarabian
21.00	TSSCOMP	10/01/2004	COMP	0	0.0	0.0 awalker1
20.00	Case Docketed	10/01/2004	COMP	30	0.0	0.0 awalker1
19.00	WROIPE	09/13/2004	COMP	20	0.0	0.0 bbustria
18.00	ROIPE	09/13/2004	WTHD	18	0.0	0.0 bbustria
17.00	WROIPE	09/13/2004	COMP	20	0.0	0.0 nnnguyen13
16.00	Complete	09/14/2004	COMP	0	0.0	0.0 nnnguyen13

Correction Action

## Related Actions

No	Short Description	Duration	Due Date	Responsible Party	Action Status
<input type="text"/>					

wstapor